4th MARC Symposium
December 8th-9th 2011

**Hasso-Plattner-Institute at University of Potsdam, Germany
in collaboration with Intel Labs Braunschweig**

http://www.marc-symposium.de

In continuation of a successful series of events, the 4th symposium of the Many-core Applications Research Community (MARC) will take place at the Hasso Plattner Institute for Software Systems Engineering in Potsdam. On December 8th and 9th, researchers from different fields will present their current and future work on many-core hardware architectures, their programming models, and the resulting research questions for the upcoming generation of heterogeneus parallel systems.

Both MARC members and interested external organizations are invited to submit unpublished reports, both on work in process or new results regarding software for novel many-core hardware architectures. While the Intel Single Chip Cloud Computer (SCC) serves as common research platform for most MARC members, other interesting research on next generation many-core platforms is also relevant for this event. Topics of interest include, but are not limited to:

- Operating system support for novel many-core architectures
- Virtualization solutions to deal with hardware limitations
- Dealing with legacy software on novel many-core architectures
- New approaches for leveraging on-die messaging facilities
- Traditional and new programming models for novel many-core hardware

All authors are invited to submit original and unpublished work as either regular papers (maximum 6 pages) for oral presentation or short papers (maximum 4 pages) for poster presentation. Papers describing work-in-progress are also welcome. Please use the IEEE document templates available on the conference web site. All accepted and presented submissions will be published as technical report of the Hasso-Plattner-Institute. The copyright remains with the authors. Paper submission is possible through EasyChair (http://www.easychair.org/conferences/?conf=marc4).

The best three papers will receive an **award from Intel**:

- First place: $1000
- Second place: $500
- Third place: $250

Submission deadline: ~~October 16th 2011, 23:59 CET~~ October 22nd, 23:59 CET
Notification of acceptance: November 13th 2011
Registration deadline: November 20th 2011
Camera-ready deadline: November 30th 2011

Organizers

General Chair: Prof. Dr. Andreas Polze, Hasso-Plattner-Institute
Program Chair: Dr. Peter Tröger, Hasso-Plattner-Institute

Program Committee

- Dr. Ulrich Bretthauer (Intel)
- Jaewoong Chung (Intel)
- Saurabh Dighe (Intel)
- Prof. Dr. Michael Gerndt (Technische Universität München)
- Diana Göhringer (Fraunhofer IOSB)
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- Dr. Michael Hübner (Karlsruhe Institute of Technology)
- Timothy M. Mattson (Intel)
- Georg Müller (Fujitsu Technology Solutions GmbH)
- Prof. Dr. Jörg Nolte (BTU Cottbus)
- Prof. Dr. Andreas Polze (Hasso-Plattner-Institute)
- Dr. Felix Salfner (SAP Innovation Center)
- Prof. Dr. Bettina Schnor (University of Potsdam)
- Prof. Dr. Theo Ungerer (Universität Augsburg)
- Dr. Peter Tröger (Hasso-Plattner-Institute)
- Dr. Daniel Versick (University of Rostock)
- Rob F. Van Der Wijngaart (Intel)

In case of any questions, don't hestitate to contact us. See you all in Potsdam !